

8. The software rewriting apparatus according to claim 5, further comprising a controller for surveying a degree of load of processing executed

by a CPU, and instructing the rewriter to carry out rewrite processing when the degree of load becomes low.

9. The software rewriting apparatus according to
5 claim 8, wherein the controller surveys the degree of load of processing executed by the CPU in response to a rewrite request sent from the rewriter.

10. A communication terminal apparatus having a software rewriting apparatus, said software
10 rewriting apparatus comprising:

a software storage for storing software having one block or a plurality of divided blocks;

a processor for expanding the blocks to be executed;

15 a block storage for temporarily storing rewriting blocks;

a discriminator for comparing the rewriting blocks with blocks executed by the processor to discriminate execution states of the blocks
20 corresponding to the rewriting blocks; and

a rewriter for performing rewrite processing in which the corresponding blocks stored in the software storage are sequentially rewritten to the rewriting blocks in accordance with a
25 discrimination result.

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